

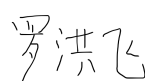
PRODUCT SPECIFICATION

CDTECH Model: **S070HWX93HN-FC117**

CUSTOMER Model: **-**

Description: **7.0" TFT-LCD Module with CTP**

Version: **1.0**

CDTECH	PREPARED BY	CHECKED BY	APPROVED BY
SIGNATURE			
DATE	2022.6.11	2022.6.11	2022.6.11

CUSTOMER APPROVAL	SIGNATURE	DATE



Record of Revisions

[illegible]



Contents

1. General Specifications.....	4
2. Absolute Maximum Ratings.....	5
3. Electrical Characteristics.....	5
4. Interface Pin Assignment.....	7
5. Interface Characteristics.....	8
6. Optical Specifications.....	15
7. Reliability Test Items.....	18
8. Mechanical Drawing.....	19
9. Packing.....	20
10. TFT-LCD Module Inspection Criteria.....	21
11. Precautions for Use of LCD modules.....	25

1. General Specifications

1.1 LCM General Information

Item	Specification	Unit
LCD Size	7.0	inch
Number of Pixels	280(H) RGB x1424(V)	pixels
Display Mode	Normally Black	-
Viewing Direction	Free	o' clock
Interface	MIPI	-
Display Colors	16.7M	colors
Outline Dimension	43.00(H) x197.00(V) x 5.23(D)	mm
Active Area	33.60(H) x 170.88(V)	mm
Pixel Pitch	0.12(H) x 0.12(V)	mm
Driver IC	OTA7290B	-
Operation Temperature	-20~70	°C
Storage Temperature	-30~80	°C

1.2 Touch Panel Information

Item	Specification
Touch Structure	G+FF
Bonding Type with LCM	OCA Optical Bonding
Driver IC	FT5446IDQQ
Interface	I ² C
Touch Count Max	5 Points
Surface treatment	-
Surface hardness	6H
I2C slave address	0x70
Origin of coordinate	Top Left Corner

Note1:Requirements on environmental protection RoHS compliant.

2. Absolute Maximum Ratings

Item	Symbol	MIN.	MAX.	Unit	Note
Analog Supply voltage	VDD	-0.3	5.0	V	Note 1

Note 1:

Permanent damage may occur to the LCD module if beyond this specification.

Functional operation should be restricted to the conditions described under normal operating conditions.

3. Electrical Characteristics

3.1 Recommended Operating Condition for TFT LCD

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Analog Supply voltage	VCI	3.0	3.3	3.6	V	
Logic input voltage	VIH	0.7*IOVCC	-	IOVCC	V	
	VIL	GND	-	0.3*IOVCC	V	

3.2 Recommended Driving Condition for Backlight

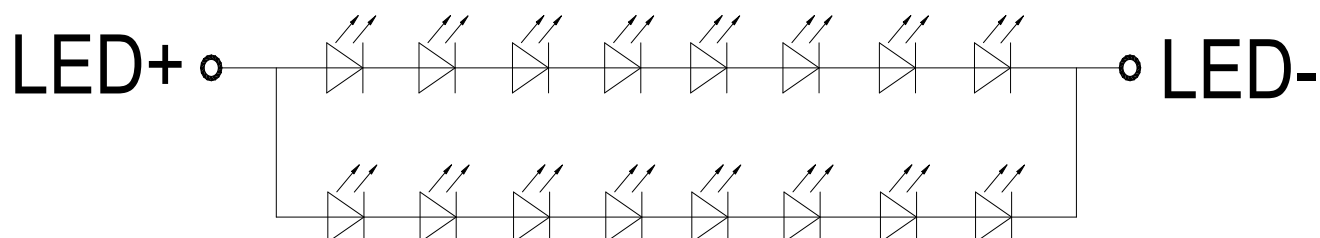
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Driving Current	I _F	-	40	-	mA	
Driving Voltage	V _F	21.6	-	27.2	V	
Power consumption	W _{BL}	0.864	-	1.088	W	
LED Life-Time	N/A	30,000	-	-	Hours	Ta=25°C Note 1

Note 1:

LED lifetime is defined as the module brightness decay 50% of original brightness at Ta=25 degree, typical current.

Note 2:

LED circuit :



3.3 Touch Panel

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Power Supply voltage	VCC	-	3.3	-	V	
Analog supply current	I _{VCC}	-	TBD	-	mA	VCC=3.3V
Input high-level voltage	VIH	0.7*VCC	-	VCC	V	
Input low -level voltage	VIL	GND	-	0.3*VCC	V	

4. Interface Pin Assignment

4.1 LCM Pin Assignment

Recommended connector: FH12-30S-0.5HS manufactured by HIROSE

No.	Symbol	Description
1	GND	GROUND
2	DN3	MIPI Negative data signal(-)
3	DP3	MIPI Positive data signal(+)
4	GND	GROUND
5	DN2	MIPI Negative data signal(-)
6	DP2	MIPI Positive data signal(+)
7	GND	GROUND
8	CKN	MIPI Negative clock signal(-)
9	CKP	MIPI Positive clock signal(+)
10	GND	GROUND
11	DN1	MIPI Negative data signal(-)
12	DP1	MIPI Positive data signal(+)
13	GND	GROUND
14	DN0	MIPI Negative data signal(-)
15	DP0	MIPI Positive data signal(+)
16	GND	GROUND
17	GND	GROUND
18	TE	Sync signal for touch panel
19	RESET	Device reset signal
20	GND	GROUND
21-23	VCI	Power supply for logic operation
24	GND	GROUND
25-26	NC	No connect
27-28	LEDK	LED Cathode
29-30	LEDA	LED anode

4.2 Touch FPC Pin Assignment

Recommended connector: FH12-6S-0.5HS manufactured by HIROSE

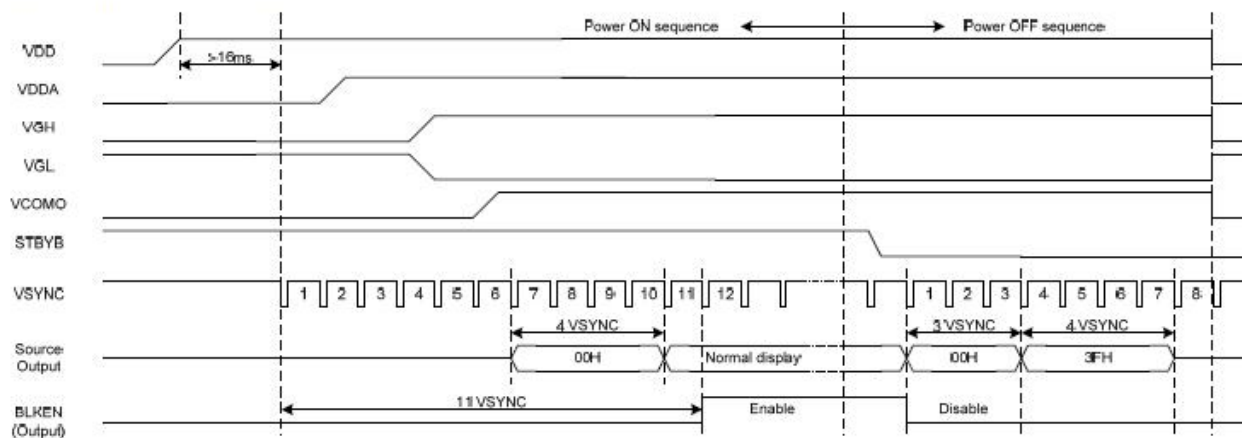
No.	Symbol	Description
1	GND	Ground
2	INT	Interrupt request to the host
3	RST	Reset Pin for CTP
4	SDA	I2C data input and output
5	SCL	I2C clock input
6	VDD	Power supply for CTP

5. Interface Characteristics

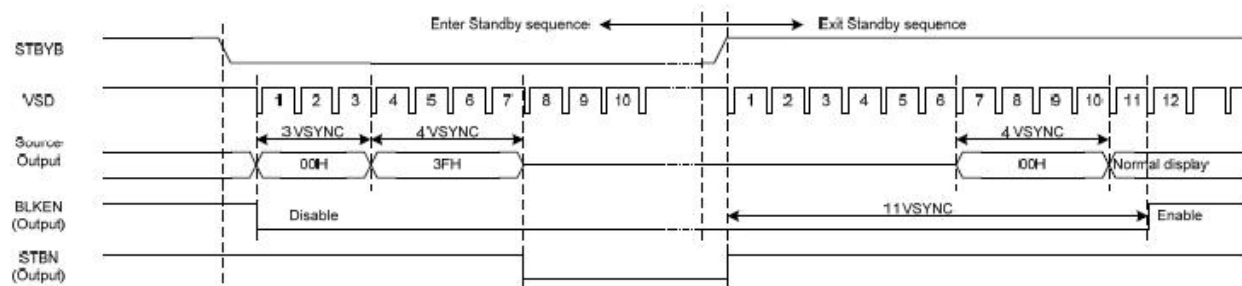
5.1 Power ON/OFF Sequence

In order to prevent IC from power on reset fail, the rising time (T_{POR}) of the digital power supply VDD should be maintained within the given specifications. Refer to "AC Characteristics" for more detail on timing.

Power-On/Off Timing Sequence:

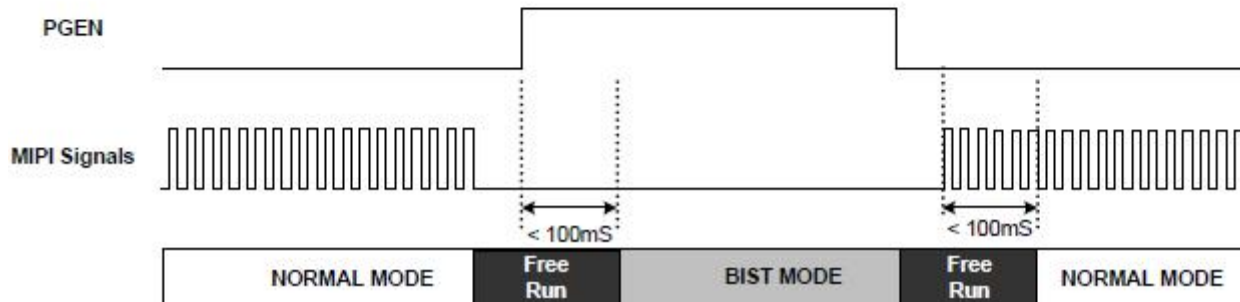


Enter and Exit Standby Mode Sequence:



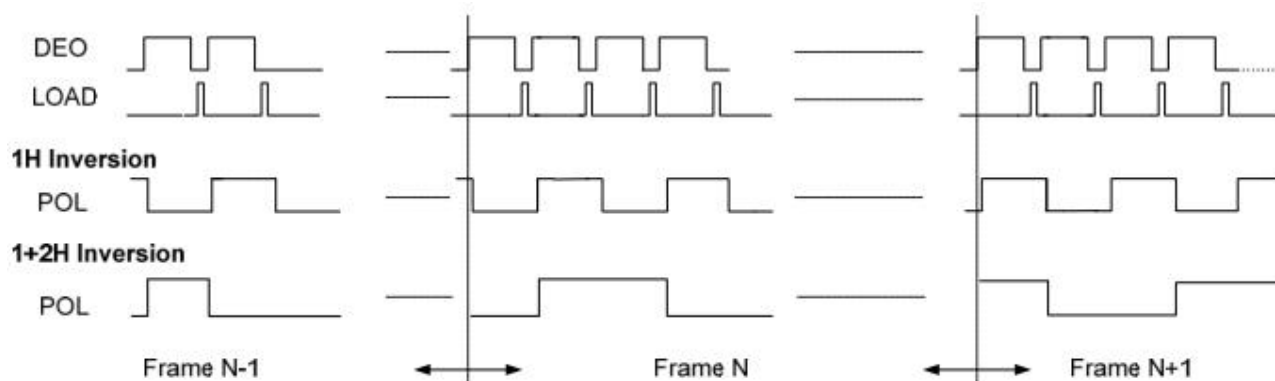
5.2 Reset Timing Characteristics

BIST Timing

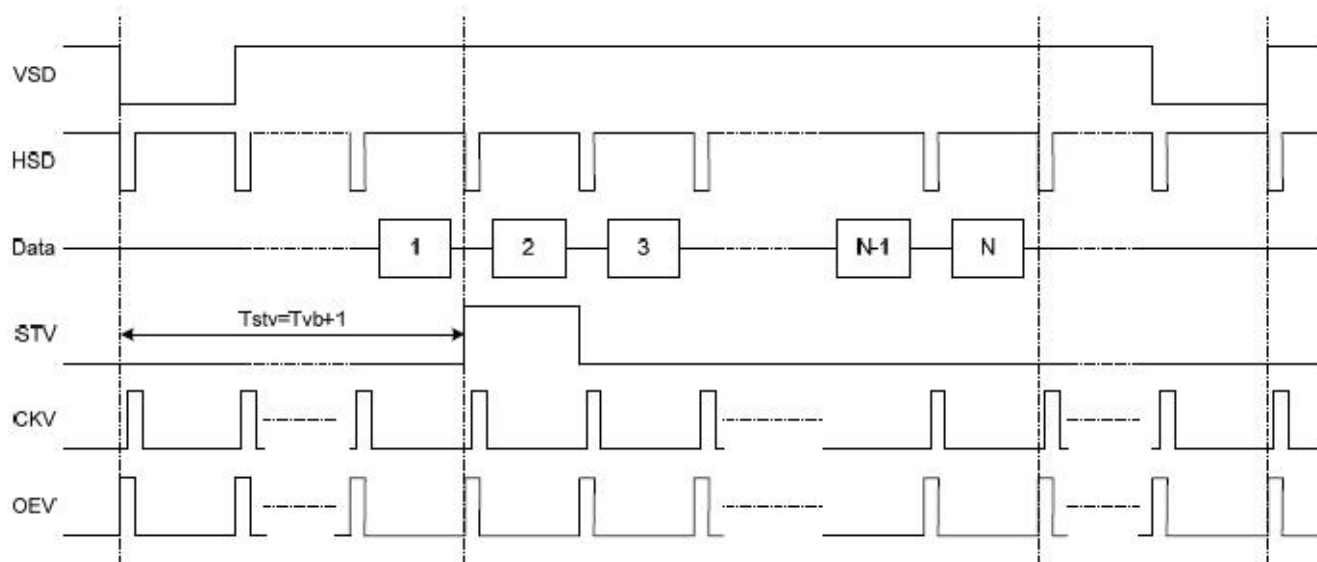


TCON Timing

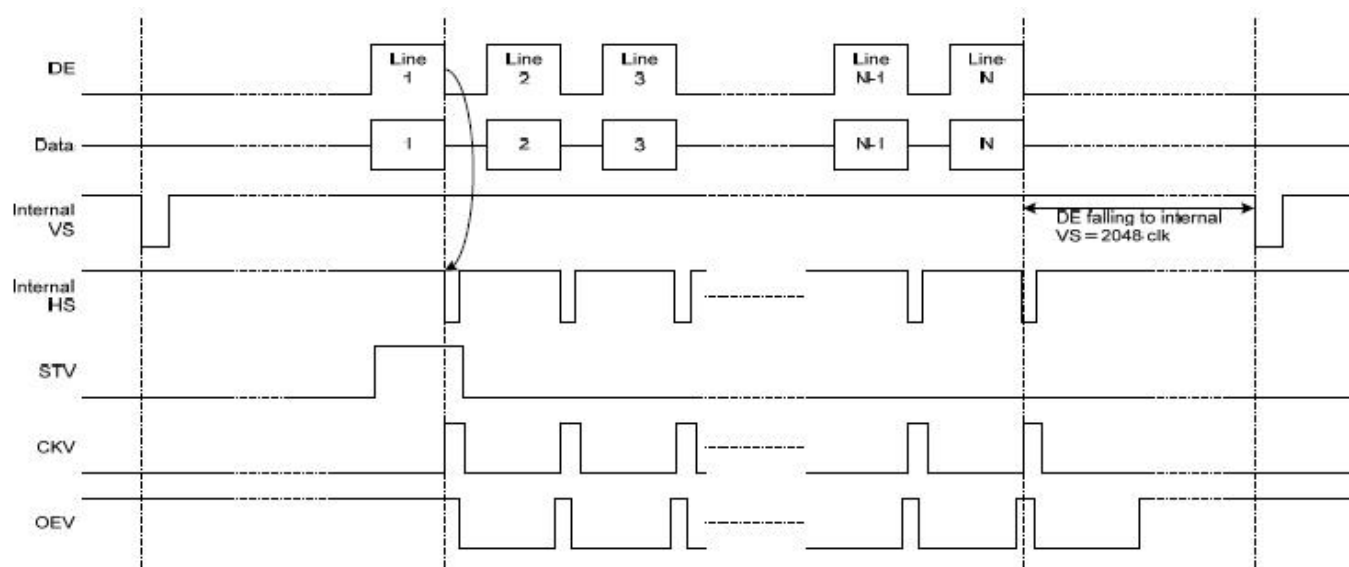
Polarity Control



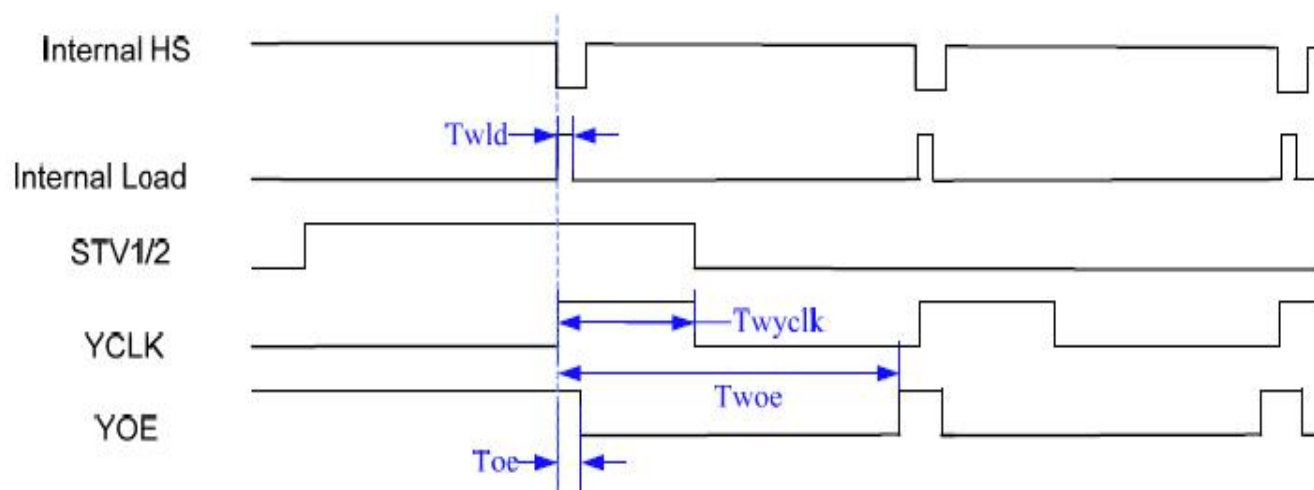
Vertical Timing Diagram HV (Cascade)



Vertical Timing Diagram DE (Cascade)



Gate output timing diagram (Cascade)



MIPI DC Characteristics

HS Receiver DC Specification

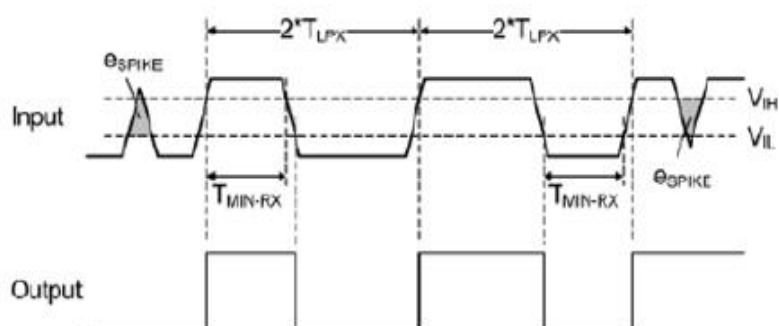
Parameter	Symbol	Rating			Unit	Note
		Min	Typ	Max		
Operation Voltage	VDD	1.5-10%	1.5	1.5+10%	mV	
Differential Input Voltage	VID	70	200	260	mV	
Common Mode Voltage	V _{CMRX(DC)}	70	-	330	mV	
Differential Input High Threshold Voltage	V _{TH}	-	-	70	mV	
Differential Input Low Threshold Voltage	V _{TL}	-70	-	-	mV	
Singled-ended input high voltage	V _{IHS}	-	-	460	mV	
Singled-ended input low voltage	V _{ILHS}	-40	-	-	mV	
Singled-ended threshold for HS termination enable	V _{TERM-EN}	-	-	450	mV	
Differential input impedance	Z _{ID}	80	100	125	ohm	
Pin leakage current	I _{LEAK}	-10	-	10	uA	
Common-mode interference beyond 450MHz	ΔV _{CMRX(HF)}	-	-	100	mV	
Common-mode interference 50MHz - 450MHz	ΔV _{CMRX(LF)}	-50	-	50	mV	
Common-mode termination	C _{CM}	-	-	60	pF	
Embedded Termination	R _T	90	100	110	ohm	2bits RT_SEL[1: 0] for termination resistor selection 00 → 200ohm 10 , 01 → 150ohm 11 → 100ohm (default) 1bit ERMEN for termination resistor enable TERMEN=0, termr disable R=(OPEN) TERMEN=1, termr enable

Note:

- (1) Excluding possible additional RF interference of 100mV peak sine wave beyond 450MHz.
- (2) This table value includes a ground difference of 50mV between the transmitter and the receiver, the static common-mode level tolerance and variations below 450MHz.

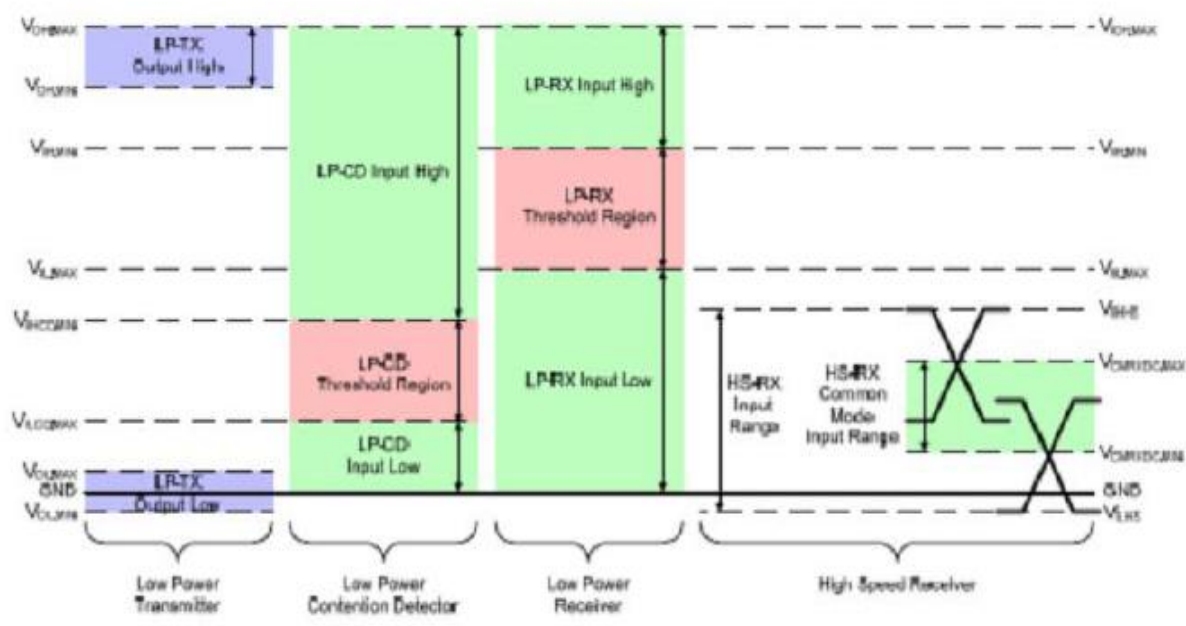
LP Receiver DC Specification

Parameter	Symbol	Rating			Unit	Note
		Min	Typ	Max		
Logic 1 input voltage	V_{IH}	880	-	-	mV	
Logic 0 input voltage, not in ULP State	V_{IL}	-	-	550	mV	
Input hysteresis	V_{HYST}	25	-	-	mV	



Line Contention Detection

Parameter	Symbol	Rating			Unit	Note
		Min	Typ	Max		
Logic 1 contention threshold	V_{IHCD}	450	-	-	mV	
Logic 0 contention threshold	V_{ILCD}	-	-	200	mV	



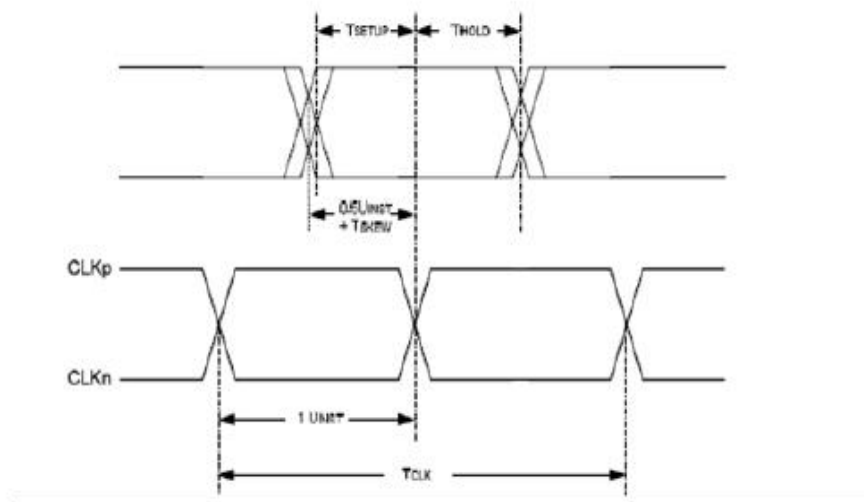
MIPI AC Chrematistics

HS Receiver AC Timing Characteristics

Parameter	Symbol	Rating			Unit	Note
		Min	Typ	Max		
Bandwidth per lane	-	-	-	1000	Mbps	Bandwidth selected by register 'speedup' Speedup=0 → Max=550Mbps Speedup=1 → Max=1000Mbps
Operation frequency	-	-	-	500	MHz	
UI instantaneous	U_{INST}	1	-	12.5	ns	1
Data to Clock Skew	T_{skew}	-0.15	-	0.15	U_{INST}	
Inter-lane static skew	$T_{skew-lane}$	-	-	$U_{INST}/50$	U_{INST}	
Data to Clock Setup Time	T_{SETUP}	0.25	-	-	U_{INST}	2
Data to Clock Hold Time	T_{HOLD}	0.25	-	-	U_{INST}	
Common-mode interference beyond 450MHz	$\Delta V_{CMRX(HF)}$	-	-	100	mV	4
Common-mode interference 50MHz- 450MHz	$\Delta V_{CMRX(LF)}$	-50	-	50	mV	3,6
Common-mode termination	C_{CM}	-	-	60	pF	5

Note:

- (1) Total silicon and package delay budget of $0.3 \cdot U_{INST}$
- (2) Total setup and hold window for receiver of $0.3 \cdot U_{INST}$
- (3) Excluding 'static' ground shift of 50mV
- (4) $\Delta V_{CMRX} (HF)$ is the peak amplitude of a sine wave superimposed on the receiver input
- (5) For higher bit rates a 14pF capacitor will be needed to meet the common-mode return loss specification.
- (6) Voltage difference compared to the DC average common-mode potential.

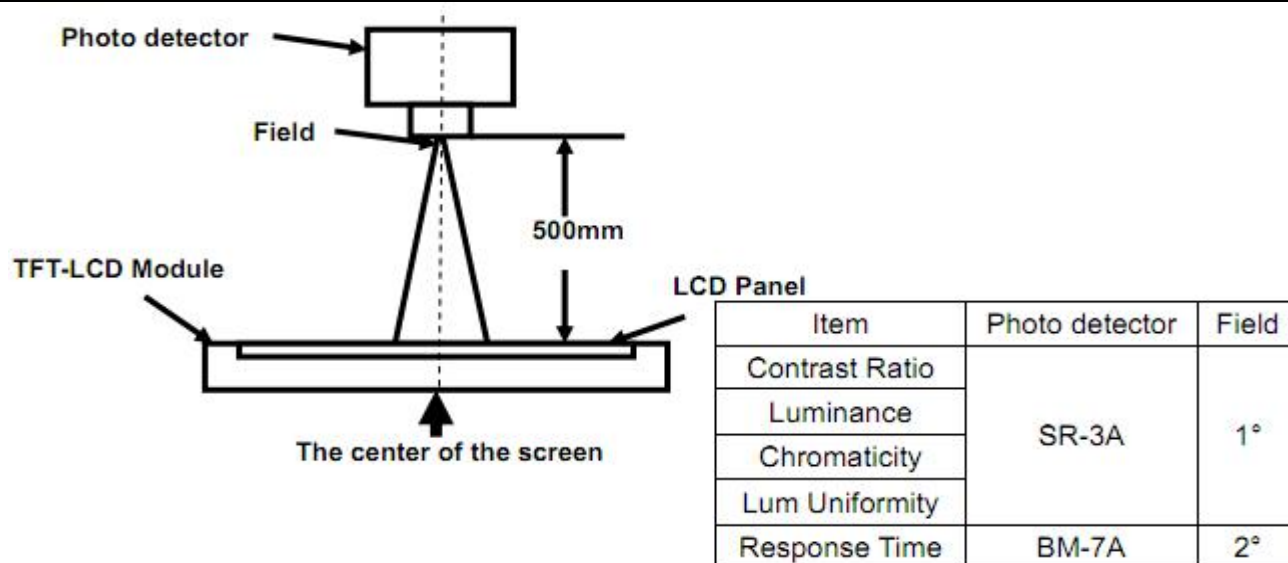


6. Optical Specifications

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Viewing Angle (CR≥10) B/L ON	θ_T	$\Phi=90^\circ$ (12 o'clock)	75	80	-	deg	Note2
	θ_B	$\Phi=270^\circ$ (6 o'clock)	75	80	-	deg	Note2
	θ_L	$\Phi=180^\circ$ (9 o'clock)	75	80	-	deg	Note2
	θ_R	$\Phi=0^\circ$ (3 o'clock)	75	80	-	deg	Note2
Response Time	T_{ON}	Normal $\theta=\Phi=0^\circ$	-	15	20	msec	Note4
	T_{OFF}		-	15	20	msec	Note4
Contrast Ratio	CR		800	1000	-	-	Note1 Note3
Color Chromaticity	W_X		TBD	TBD	TBD	-	Note1 Note5
	W_Y		TBD	TBD	TBD	-	Note1 Note5
Luminance	L		400	500	-	cd/m ²	Note1 Note7
Luminance Uniformity	Y_U		75	80	-	%	Note1 Note6
NTSC	-		-	60	-	%	-

Note 1:Definition of optical measurement system

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Note 2: Definition of viewing angle range and measurement system

viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).

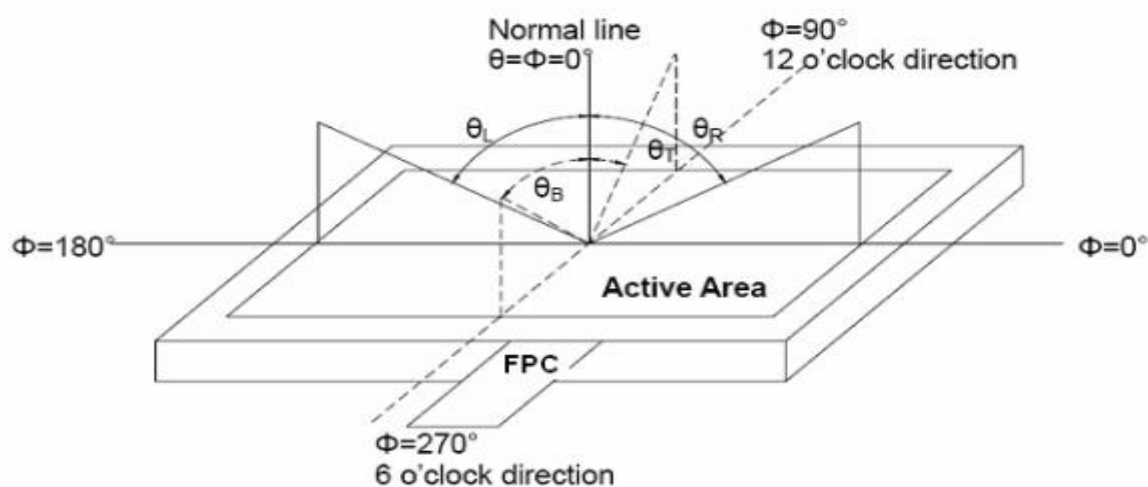


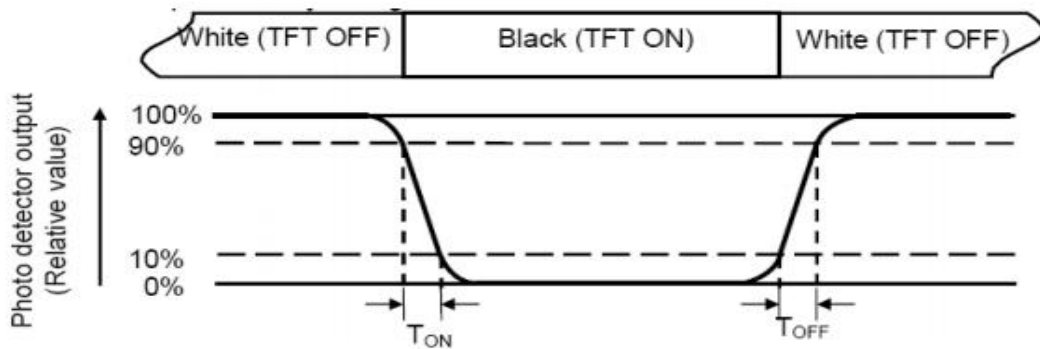
Fig. 1 Definition of viewing angle

Note 3: Definition of contrast ratio

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

The luminance uniformity in surface luminance is determined by measuring luminance at each test position 1 through n, and then dividing the maximum luminance of n points luminance by minimum luminance of n points luminance. For more information see FIG.2.

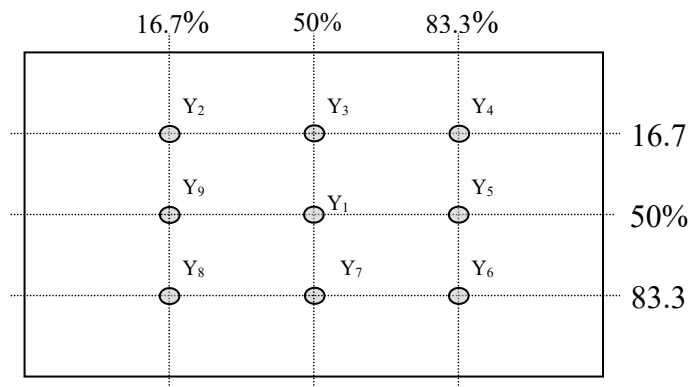


Fig. 2 Definition of points

Note 7: Definition of Luminance (Refer Fig. 2)

Surface luminance is the luminance with all pixels displaying white.

L_v = Average Surface Luminance with all white pixels($P_1, P_2, P_3, \dots, P_n$).

7. Reliability Test Items

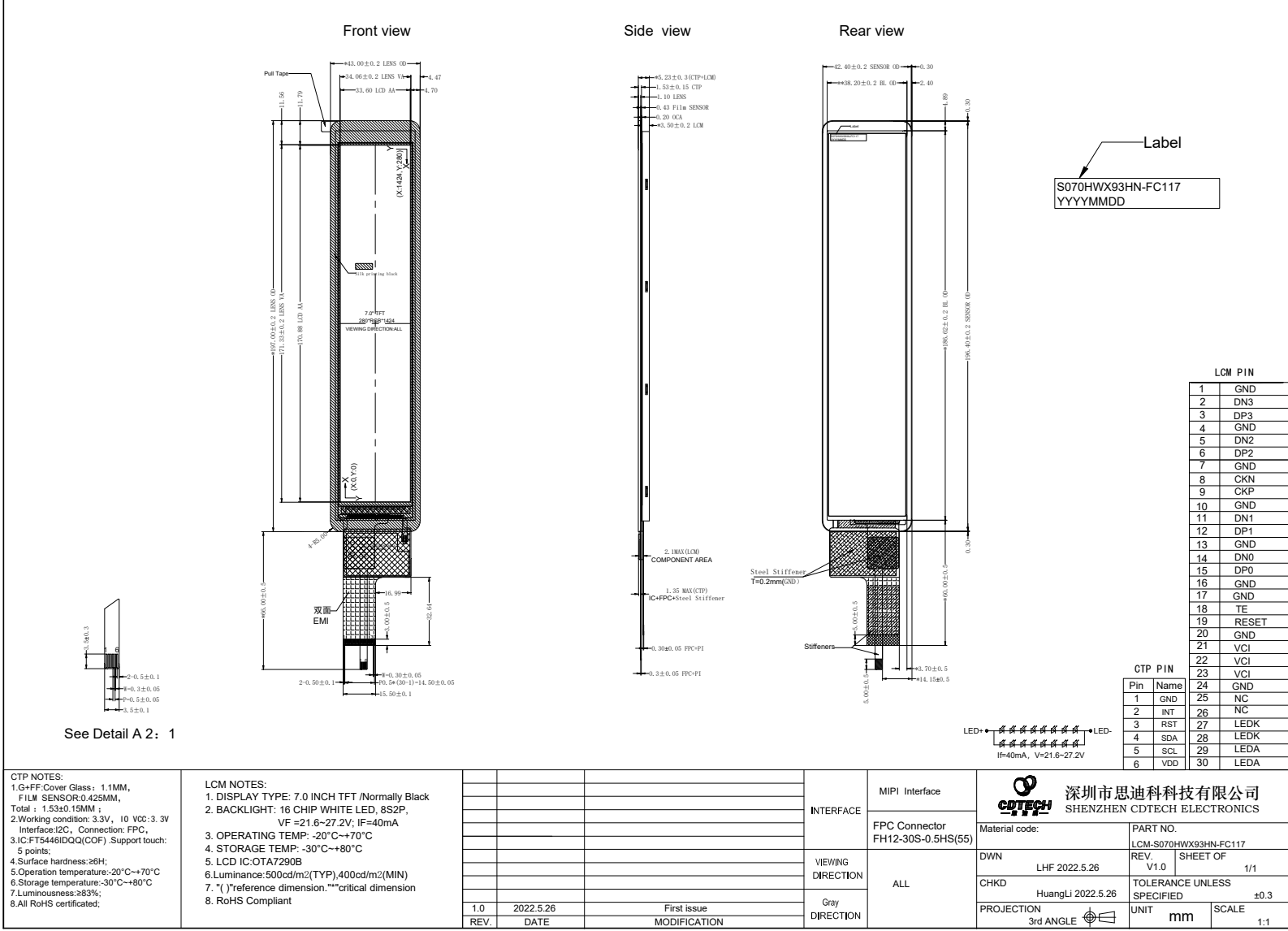
Test Item	Test Conditions
High Temperature Storage	Ta= +80℃ 96hrs
Low Temperature Storage	Ta= -30℃ 96hrs
High Temperature Operation	Ta= +70℃ 96hrs
Low Temperature Operation	Ta= -20℃ 96hrs
High Temperature and Humidity Operation	Ta= +60℃, 90% RH 96hrs
Thermal Shock (Non-operation)	-30℃/30 min ~ +80℃/30 min for 20 cycles Start with cold temperature, end with high temperature
Electro Static Discharge	Contact = ± 4 kV, class B Air = ± 8 kV, class B R=330Ω,C=150pF
Vibration	Sweep: 10Hz~55Hz~10Hz Stroke: 1.5mm 2 hrs for each direction of X .Y. Z.
Mechanical Shock	60G 6ms,±X,±Y,±Z 3 times for each direction
Package Drop Test	Height: 60 cm, 1 corner, 3 edges, 6 surfaces

Notes:

1. The test result shall be evaluated after the sample has been left at room temperature and humidity for 2 hours without load. No condensation shall be accepted. The sample will not be accepted if appear these defects:

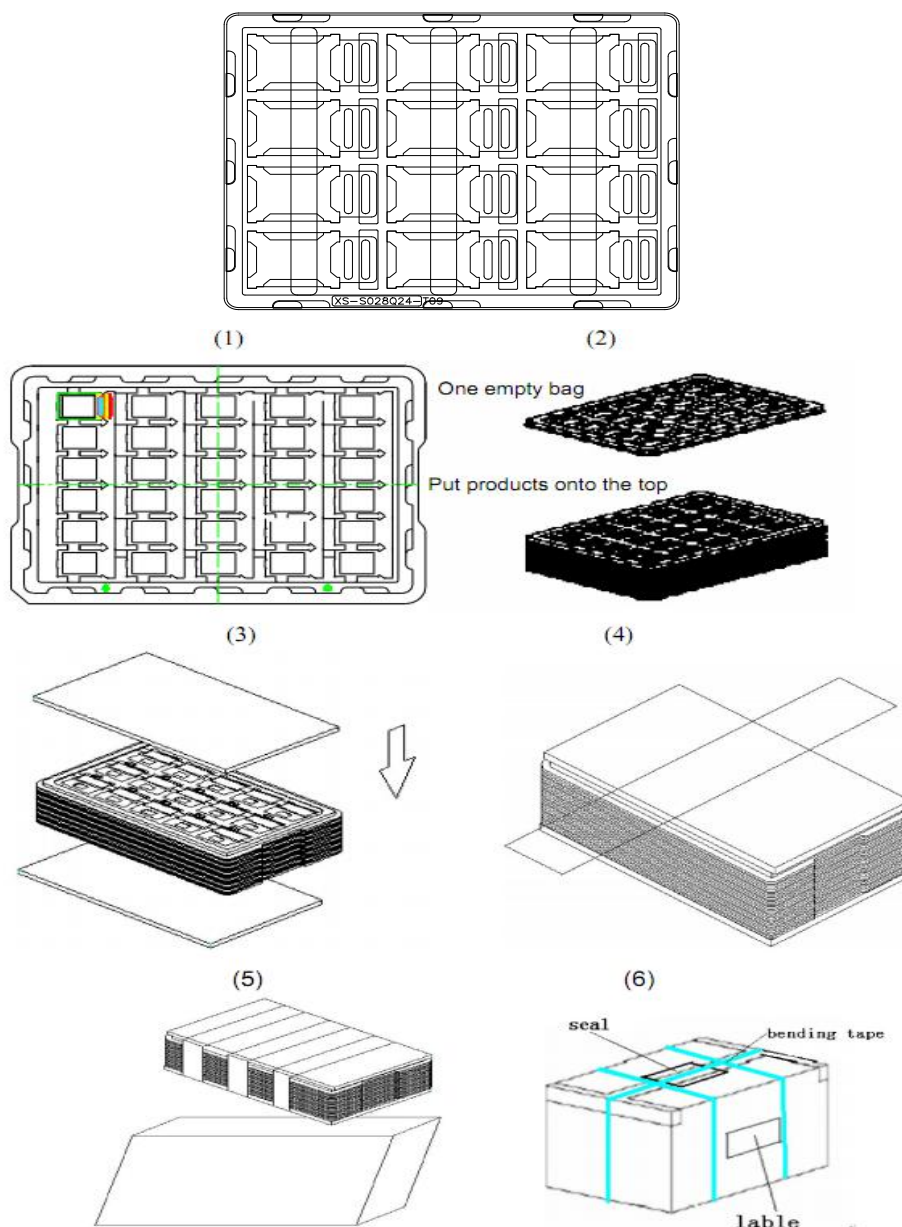
- 1).Air bubble in the LCD
- 2).Seal leak or Glass crack
- 3).Non display or abnormal display
- 4).Brightness reduction >50%

8. Mechanical Drawing



9. Packing

Packing Method



Steps:

1. Put module into tray cavity
2. Tray stacking
3. Put 1 cardboard under the tray stack and 1 cardboard above
4. Fix the cardboard to the tray stack with adhesive tape
5. Put the tray stack into carton
6. Carton sealing with adhesive tape

10. TFT-LCD Module Inspection Criteria

10.1 Scope

The incoming inspection standards shall be applied to TFT –LCD Modules (hereinafter called "Modules") that supplied by CDTech.

10.2 Incoming Inspection

The customer shall inspect the modules within twenty calendar days of the delivery date (the "inspection period") at its own cost. The result of the inspection (acceptance or rejection) shall be recorded in writing, and a copy of this writing will be promptly sent to the seller, If the results of the inspecting from buyer does not send to the seller within twenty calendar days of the delivery date. The modules shall be regards as acceptance.

Should the customer fail to notify the seller within the inspection period, the buyers right to reject the modules shall be lapsed and the modules shall be deemed to have been accepted by the buyer.

10.3 Inspection Sampling

10.3.1. Lot size: Quantity per shipment lot per model

10.3.2. Sampling type: Normal inspection, Single sampling

10.3.3. Inspection level: II

10.3.4. Sampling table: MIL-STD-105E

10.3.5. Acceptable quality level (AQL)

Major defect: AQL=0.65

Minor defect: AQL=1.00

10.4 Inspection Conditions

10.4.1 Ambient conditions:

a. Temperature: Room temperature $25\pm 5^{\circ}\text{C}$

b. Humidity: $(60\pm 10)\% \text{RH}$

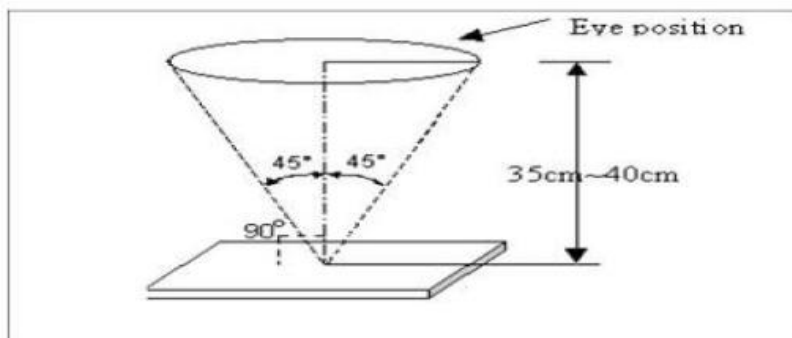
c. Illumination: Single fluorescent lamp non-directive (300 to 700 Lux)

10.4.2 Viewing distance

The distance between the LCD and the inspector's eyes shall be at least $35\pm 5\text{ cm}$.

10.4.3 Viewing Angle

U/D: $45^{\circ}/45^{\circ}$, L/R: $45^{\circ}/45^{\circ}$



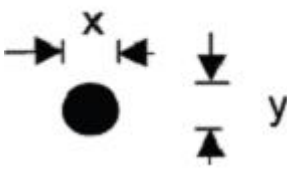
10.5 Inspection Criteria

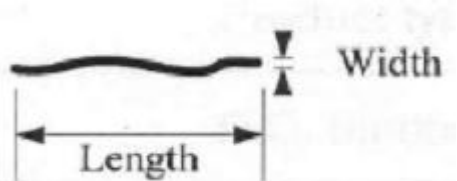
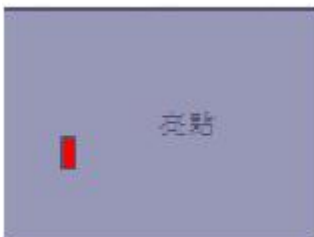
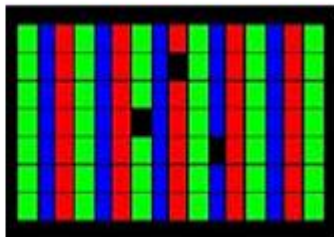
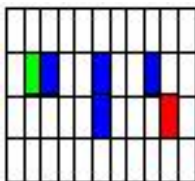
Defects are classified as major defects and minor defects according to the degree of Defectiveness defined here in.

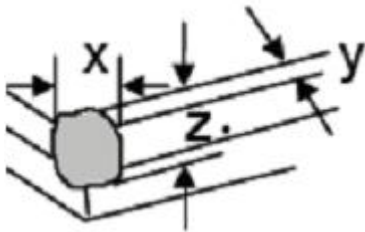
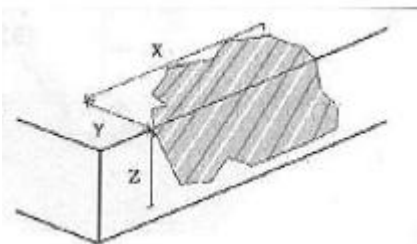
10.5.1 Major defect

Item No	Items to be inspected	Inspection Standard
5.1.1	All functional defects	1) No display 2) Display abnormally 3) Short circuit 4) line defect
5.1.2	Missing	Missing function component
5.1.3	Crack	Glass Crack

10.5.2 Minor defect

Item No	Items to be inspected	Inspection standard	
5.2.1	Spot Defect Including Black spot White spot Pinhole Foreign particle Polarizer dirt	For dark/white spot is defined $\varphi = (x+y) / 2$ 	
		Size φ (mm)	Acceptable Quantity
		$\varphi \leq 0.2$	Ignore
		$0.2 < \varphi \leq 0.35$	3
		$0.35 < \varphi$	Not allowed

5.2.2	Line Defect Including Black line White line Scratch	Define: 	
		Width(mm) Length(mm)	Acceptable Quantity
		$W \leq 0.05$	Ignore
		$0.05 < W \leq 0.1$ $L \leq 2.5$	4
		$0.1 < W, \text{ or } L > 2.5$	Not allowed
5.2.3	Polarizer Dent/Bubble	Size ϕ (mm)	Acceptable Quantity
		$\phi \leq 0.2$	Ignore
		$0.2 < \phi \leq 0.35$	3
		$0.35 < \phi$	Not allowed
		Total QTY	3
5.2.4.	Electrical Dot Defect	Bright and Black dot define:  and   Two Adjacent Dot	
		Inspection pattern: Full white 、 Full black 、 Red 、 green and blue screens	
		Item	Acceptable Quantity
		Black dot defect	3
		Bright dot defect	2
		Total Dot	3

5.2.5	Glass defect		
		1. Corner Fragment:	
		Size(mm)	Acceptable Quantity
		X≤3mm Y≤1mm Z≤T	Ignore T: Glass thickness X: Length Y: Width Z: thickness
		2. Side Fragment:	
			
		Size(mm)	Acceptable Quantity
		X≤5.0mm Y≤1mm Z≤T	T: Glass thickness X: Length Y: Width Z: thickness

- Note:
- 1). Dot defect is defined as the defective area of the dot area is larger than 50% of the dot area.
 - 2). The distance between two bright dot defects (red, green, blue, and white) should be larger than 15mm.
 - 3). The distance between black dot defects or black and bright dot defects should be more than 5mm apart.
 - 4). Polarizer bubble is defined as the bubble appears on active display area. The defect of polarizer bubble shall be ignored if the polarizer bubble appears on the outside of active display area.

10.6 Mechanics specification

As for the outside dimension, weight of the modules, please refer to product specification For more details

11. Precautions for Use of LCD modules

11.1 Handling Precautions

11.1.1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

11.1.2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

11.1.3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

11.1.4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

11.1.5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketene
- Aromatic solvents

11.1.6. Do not attempt to disassemble the LCD Module.

11.1.7. If the logic circuit power is off, do not apply the input signals.

11.1.8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

11.1.8.1. Be sure to ground the body when handling the LCD Modules.

11.1.8.2. Tools required for assembly, such as soldering irons, must be properly ground.

11.1.8.3. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

11.1.8.4. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

11.2 Storage Precautions

11.2.1. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

11.2.2. The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ~ 40℃ Relatively humidity: ≤80%

11.2.3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

11.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.