深圳市思迪科科技有限公司
SHENZHEN CDTECH ELECTRONICS

Product Specification

Model Name	S101HWX33HD
Description	1280(RGB)x800 Dots 10.1" TFT LCD
Date	2020/05/16
Revision	1.0

Approved by/Date	**Check by/Date**	**Prepared by/Date**
ZHP 2020/05/16	HZX 2020/05/16	Yigui.Han 2020/05/16

Customer Approval	
Date	

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1 Record of Revision

Rev	Issued Date	Description	Editor
1.0	2020/05/16	First Release.	ZHP

2 General Specifications

Feature		Spec
Characteristics	Size	10.1 inch
	Resolution	1280(Horizontal)*800(Vertical)
	Interface	LVDS
	Connect type	Connector
	Color Depth	16.7M
	Technology type	a-Si
	Display Spec. Pixel pitch (mm)	0.1695(H)×0.1695(V)
	Pixel Configuration	R.G.B. Vertical Stripe
	Display Mode	Normally Black
	Driver IC	EK79202
	Viewing Direction	ALL
Mechanical	LCM (W x H x D) (mm)	229.46(W)*149.10(H)*4.5 (D)
	Active Area(mm)	216.96(H)x 135.60(V)
	With /Without TP	Without TP
	Weight (g)	TBD
	LED Numbers	45 LEDs

Note 1: Requirements on Environmental Protection: RoHS
Note 2: LCM weight tolerance: +/- 5%

3 Input/Output Terminals

No.	Symbol	Description	Note
1	VCOM(-0.65V)	Common voltage	
2-3	VDD（3.3V）	Power Supply	
4	NC	No connection	
5	RESET	Reset Pin for LCM	
6	STBYB	Standby mode, normally pull high STBYB=”1”, normal operation STBYB=”0”, source driver will turn off, all output are high-Z	
7	GND	Ground	
8	RXIN0-	- LVDS differential data input	
9	RXIN0+	+ LVDS differential data input	
10	GND	Ground	
11	RXIN1-	- LVDS differential data input	
12	RXIN1+	+ LVDS differential data input	
13	GND	Ground	
14	RXIN2-	- LVDS differential data input	
15	RXIN2+	+ LVDS differential data input	
16	GND	Ground	
17	RXCLK-	- LVDS differential clock input	
18	RXCLK+	+ LVDS differential clock input	
19	GND	Ground	
20	RXIN3-	- LVDS differential data input	
21	RXIN3+	+ LVDS differential data input	
22	GND	Ground	
23-24	NC	No connection	
25	GND	Ground	
26	NC	No connection	
27	LVBIT	6bit/8bit mode select	Note 1
28	NC	No connection	
29	VSP(5.5V)	Input voltage from the set-up circuit (5.5V).	
30	NC	No connection	
31-32	LEDK	Power for LED backlight (Cathode)	
33	NC	No connection	
34	VSN(-5.5V)	Input voltage from the set-up circuit (-5.5V).	
35	NC	No connection	
36	VGL(-10V)	Gate OFF Voltage	

37	NC	No connection	
38	VGH(18V)	Gate ON Voltage	
39-40	LEDA	Power for LED backlight (Anode)	

Note 1: if LVDS input data is 8bit,selb must be set to high;
if LVDS input data is 6bit,selb must be set to low;

4 Absolute Maximum Ratings

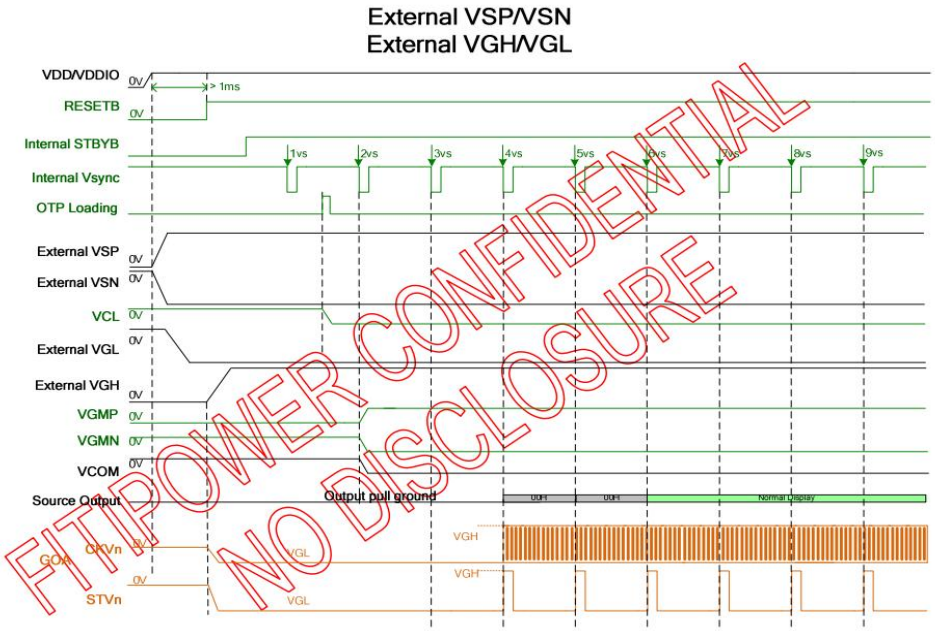
Item	Symbol	MIN	Typ	MAX	Unit	Remark
Supply Voltage	VDD	-0.3	-	5.0	V	-
Operating Temperature	TOPR	-20	-	70	℃	-
Storage Temperature	TSTG	-30	-	80	℃	

5 Electrical Characteristics

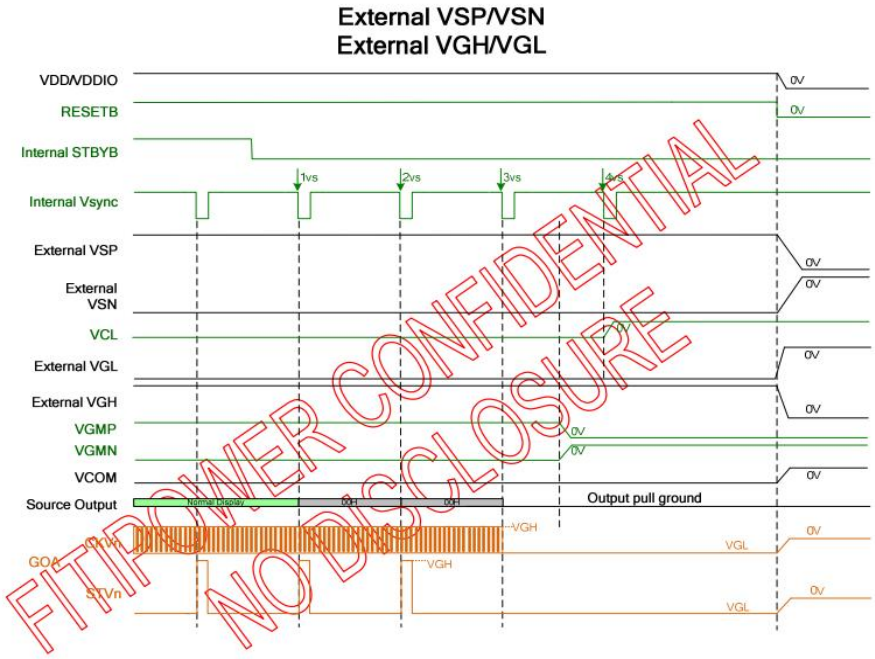
5.1 Driving TFT LCD Panel

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Power voltage	VDD	3.0	3.3	3.6	V	
	VGH	16	18.0	20	V	
	VGL	-11	-10	-8	V	
	VSN	-6.0	-5.5	-4.5		
	VSP	4.5	5.5	6.0		
Input signal voltage	VCOM	-1.5	-0.65	-0.5	V	

5.2 POWER ON/OFF SEQUENCE



Power on sequence with PMODE=H



Power off sequence with PMODE=H

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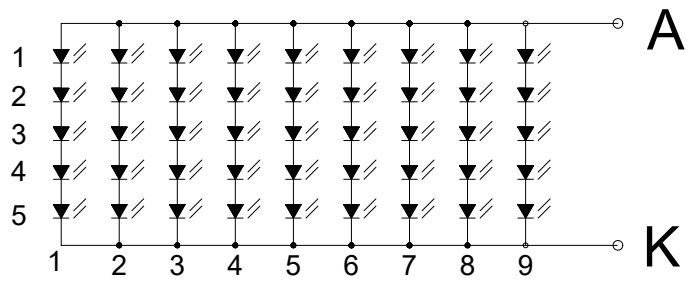
5.3 Driving Backlight

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Forward Current	I_F	-	270	-	mA	
Forward Voltage	V_F	14V	16V	18V	V	
Backlight Power consumption	W_{BL}	-	4.32	-	W	
LED Lifetime		-	50000	-	Hrs	

Note 1: Each LED: IF =30 mA, VF =3.2+/0.2V.
Note 2: Optical performance should be evaluated at Ta=25℃ only.
Note 3: If LED is driven by high current, high ambient temperature & humidity condition. The life Time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.



CIRCUIT DIAGRAM

5.4 RESET TIMING CHARACTERISTICS

When RESETB of the reset pin equals to Low, it will be in the condition of reset. When it is in the condition of reset, it will make the device recover the initial set.

However, in order to avoid the reset noise cause reset, there is a mechanism to judge about whether the reset is needed or not.

The closed interval of Low can be shown as the following.

(Test condition: VDDIO=2.3V~3.6V, VSS=0V, T_A=-20 ~+85)

Parameter	Symbol	Conditions	Spec.			Unit
			Min.	Typ.	Max	
Reset low pulse width	Trst		20	-	-	μs

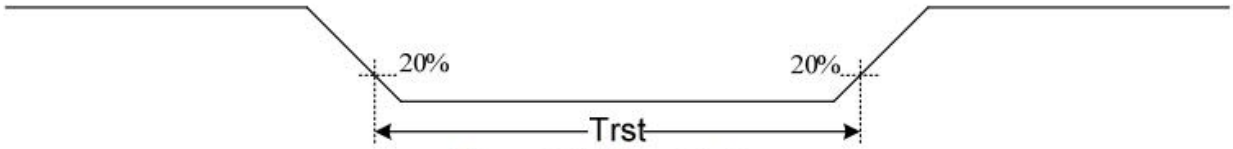
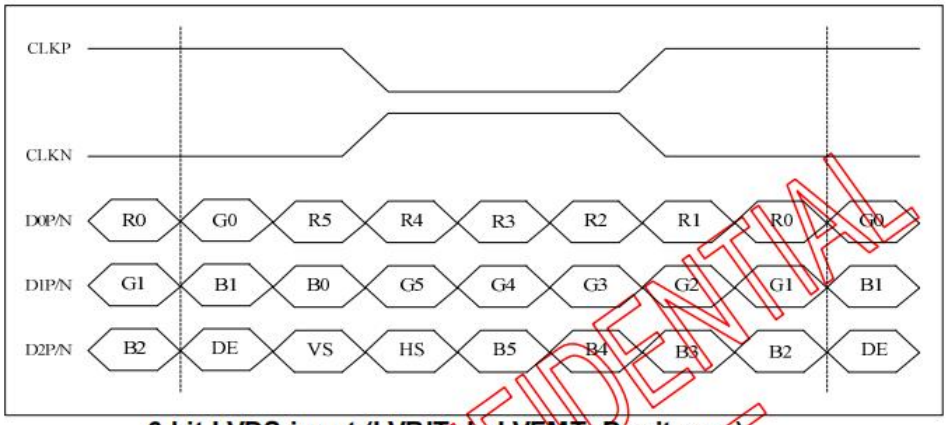
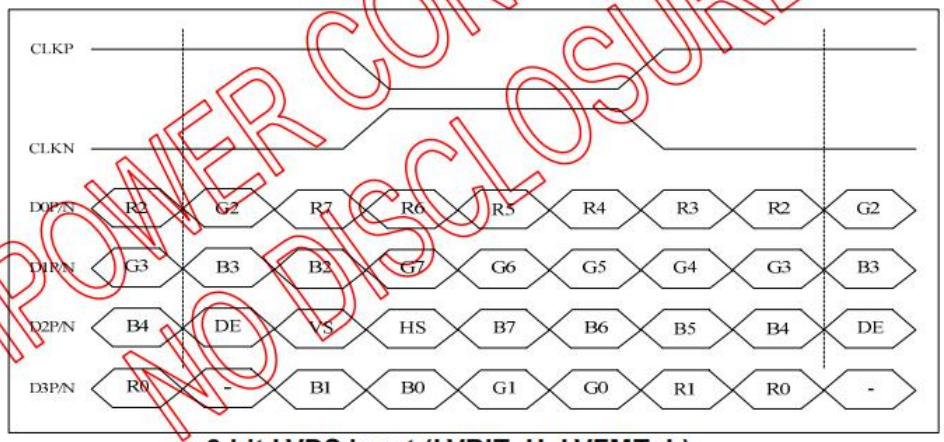


Figure 13.5: Reset timing

5.5 LVDS interface CHARACTERISTCS



6-bit LVDS input (LVBIT=L, LVFMT=Don't care)



8-bit LVDS input (LVBIT=H, LVFMT=L)

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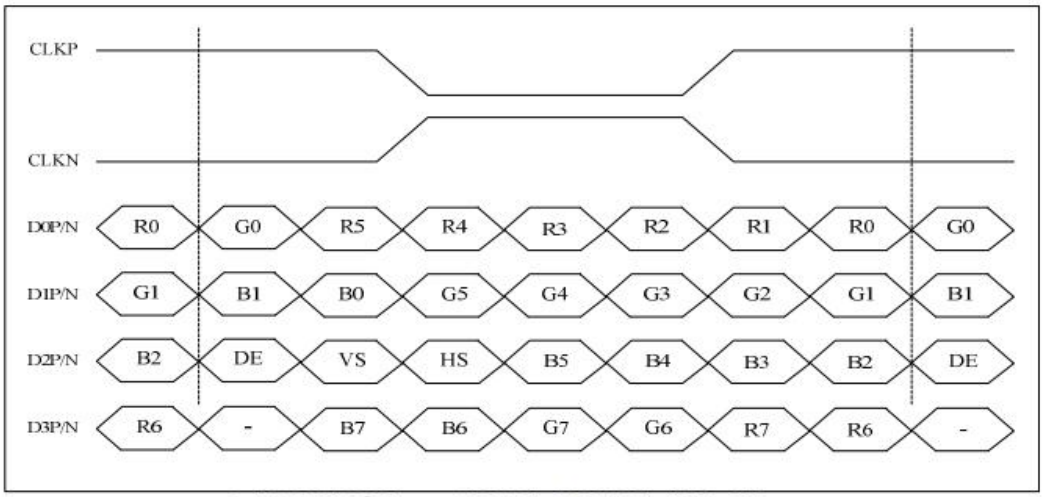


8-bit LVDS input(LVBIT=H, LVFMT=H)

For 1280RGBx800

Parameter		Symbol	Value			Unit
			Min.	Typ.	Max.	
DCLK frequency @Frame rate=60Hz (LVDS)		F_{DCLK}	66.3	72.4	78.9	MHz
HSYNC period time		T_H	1380	1440	1500	DCLK
Horizontal display area		T_{HD}		1280		DCLK
HSYNC pulse width	Min.	T_{HPW}		1		
	Typ.			-		
	Max.			40		
HSYNC back porch(with pulse width)		T_{HBP}	88	88	88	DCLK
HSYNC front porch		T_{HFP}	12	72	132	DCLK
VSYNC period time		T_V	824	838	872	H
Vertical display area		T_{VD}		800		H
VSYNC pulse width	Min.	T_{VPW}		1		H
	Typ.			-		
	Max.			20		
VSYNC back porch(with pulse width)		T_{VBP}	23	23	23	H
VSYNC front porch		T_{VFP}	1	15	49	H

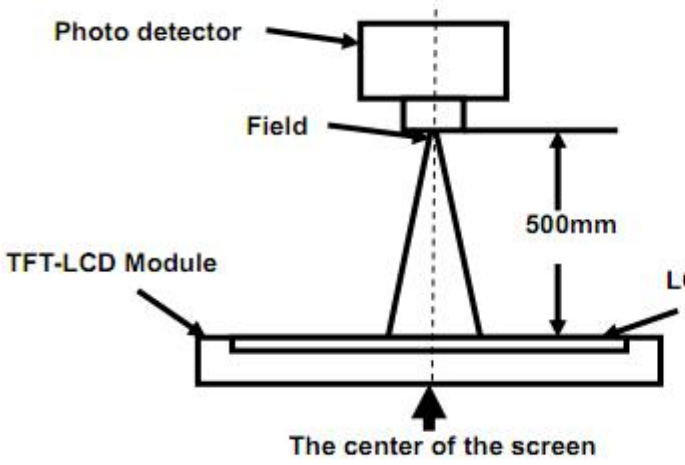
6 Optical Characteristics

Items		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing angles		θ_T	Center CR≥10		80	-	Degree.	Note2
		θ_B			80	-		
		θ_L			80	-		
		θ_R			80	-		
Contrast Ratio		CR	Θ =0	800	1000	-	-	Note1, Note3
Response Time		T_{ON}	25°C	-	25	35	ms	Note1, Note4
		T_{OFF}						
Chromaticity	White	X_W	Backlight is on	-	0.322	-	-	Note1, Note5
		Y_W		-	0.344	-	-	
Luminance Uniformity		LU		70	75	-	%	Note1, Note6
Luminance		L		450	500		cd/m2	Note1, Note7

Test Conditions:
1. IF= 30mA(one channel),the ambient temperature is 25°C.
2. The test systems refer to Note 1 and Note 2.

Note 1:Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo detector	Field
Contrast Ratio	SR-3A	1°
Luminance		
Chromaticity		
Lum Uniformity		
Response Time	BM-7A	2°

Note 2: Definition of viewing angle range and measurement system.
viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).

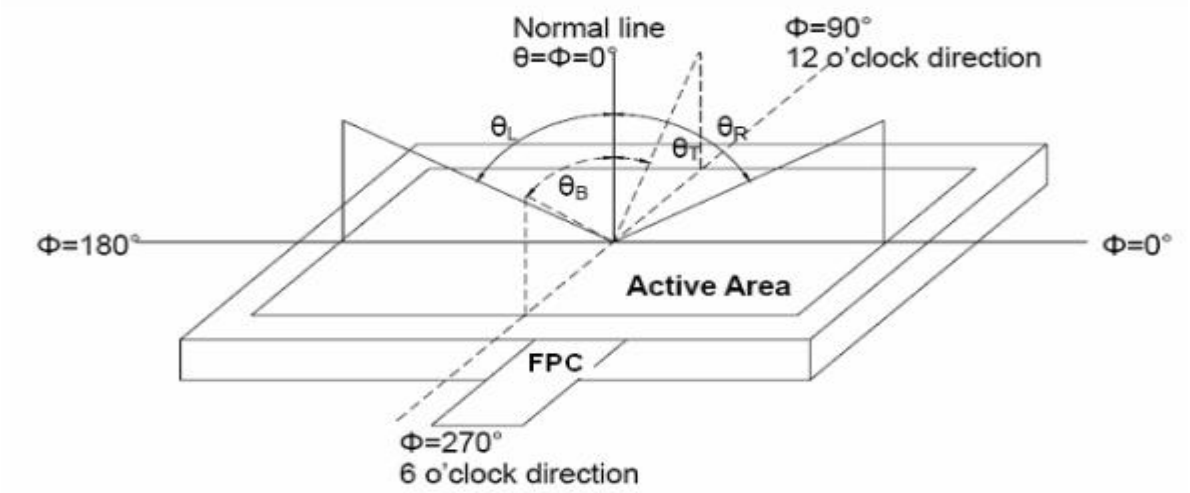


Fig. 1 Definition of viewing angle

Note 3: Definition of contrast ratio

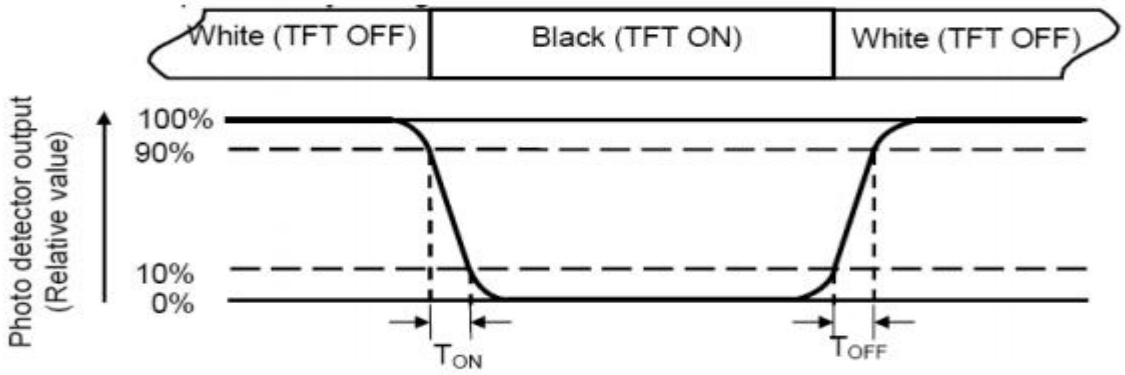
$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

“White state “:The state is that the LCD should driven by Vwhite.

“Black state”: The state is that the LCD should driven by Vblack.

Vwhite: To be determined Vblack: To be determined.

Note 4: Definition of Response time
The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)
Color coordinates measured at center point of LCD.
Note 6: Definition of Luminance Uniformity
Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity(U) = Lmin/ Lmax X100%
L-------Active area length W----- Active area width

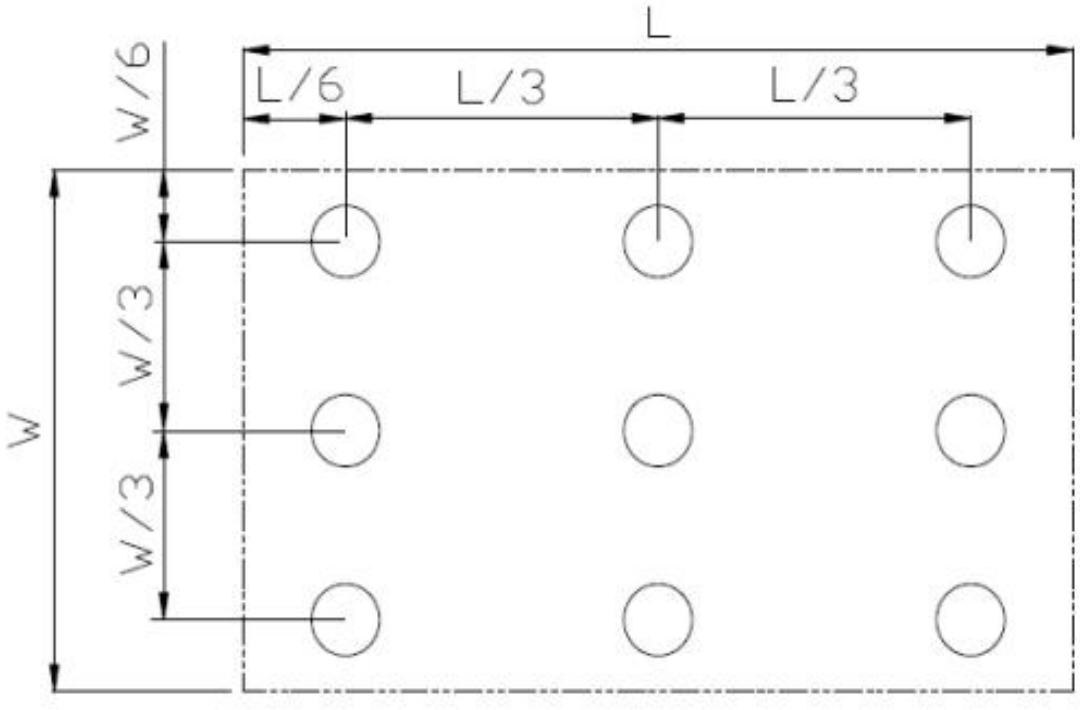


Fig. 2 Definition of uniformity
Lmax: The measured maximum luminance of all measurement position.
Lmin: The measured minimum luminance of all measurement position.

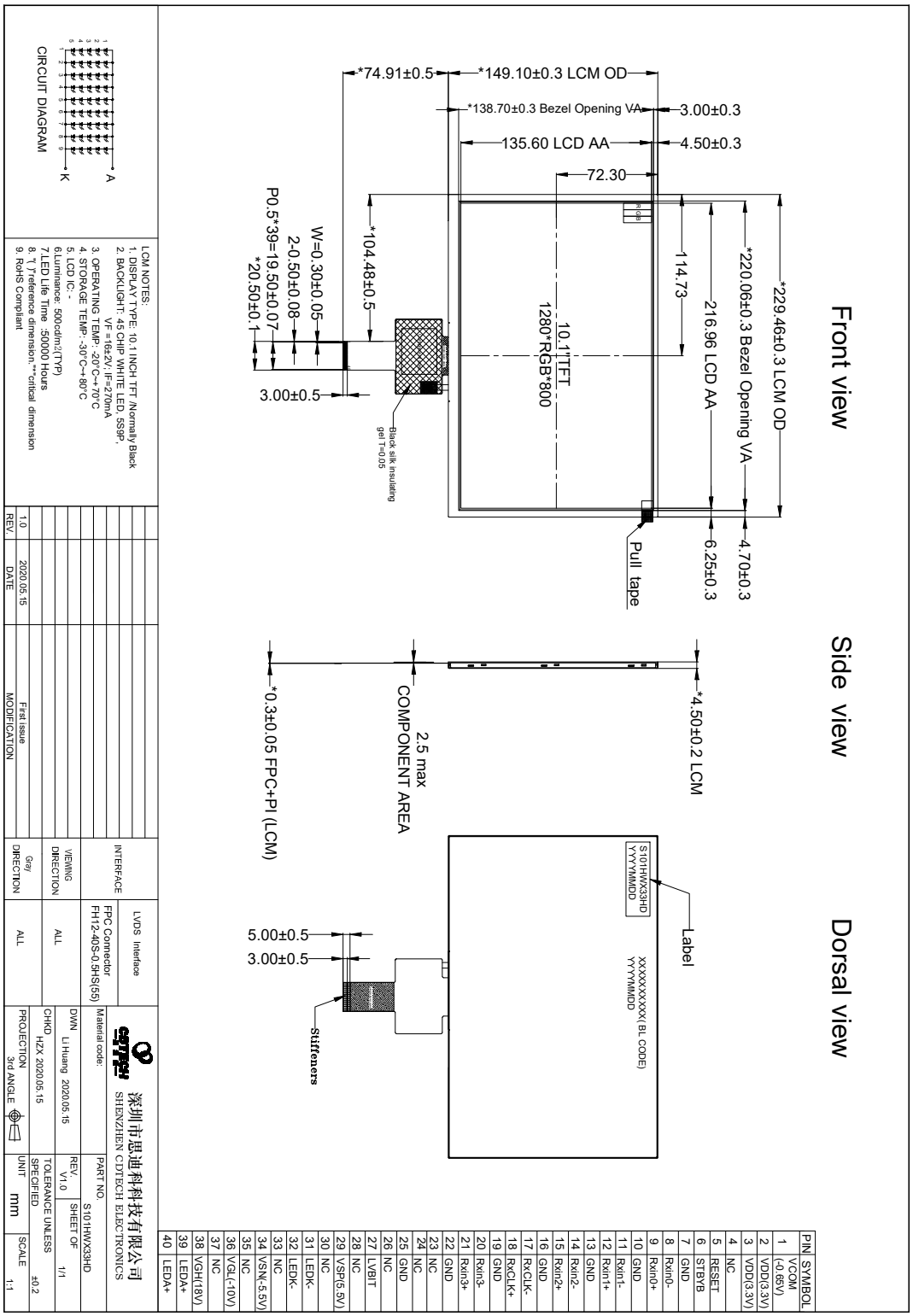
Note 7: Definition of Luminance :
Measure the luminance of white state at center point.

7 Environmental / Reliability Tests

No	Test Item	Condition	Remarks
1	High Temperature Opeartion	Ts= +70℃, 96hrs	Note 1 IEC60068-2-2, GB2423. 2-89
2	Low Temperature Opeartion	Ta= -20℃, 96hrs	Note 2 IEC60068-2-1 GB2423.1-89
3	High Temperature Storage	Ta= +80℃, 96hrs	IEC60068-2-2 GB2423. 2-89
4	Low Temperature Storage	Ta= -30℃, 96hrs	IEC60068-2-1 GB/T2423.1-89
5	High Temperature & Humidity Storage	Ta= +60℃, 90% RH max, 96 hours	IEC60068-2-3 GB/T2423.3-2006
6	Thermal Shock (Non-operation)	-30℃ 30 min ~ +80℃ 30 min Change time: 5min, 30 Cycle	Start with cold temperature,end with high temperature IEC60068-2-14, GB2423.22-87
7	Electro Static Discharge (Opeartion)	C=150pF, R=330 Ω, 5 points/panel Air:±8KV, 5 times; Contact: ±4KV, 5 times; (Environment: 15℃ ~ 35℃, 30% ~ 60%, 86Kpa ~ 106Kpa)	IEC61000-4-2 GB/T17626.2-1998
8	Vibration (Non-operation)	Frequency range: 1~55Hz, Stroke: 1.mm, Sweep: 1Hz~55Hz~3.5Hz 2 hours for each direction of X .Y. Z. (package condition)	IEC60068-2-6 GB/T2423.5-1995
10	Package Drop Test	Height: 80 cm, 1 corner, 3 edges, 6 surfaces	IEC60068-2-32 GB/T2423.8-1995

Note: 1. T_S is the temperature of panel's surface.
2. Ta is the ambient temperature of sample.

8 Mechanical Drawing



PIN	SYMBOL
1	VCOM (-0.65V)
2	VDD(3.3V)
3	VDD(3.3V)
4	NC
5	RESET
6	STBYB
7	GND
8	RxIn0-
9	RxIn0+
10	GND
11	RxIn1-
12	RxIn1+
13	GND
14	RxIn2-
15	RxIn2+
16	GND
17	RxCLK-
18	RxCLK+
19	GND
20	RxIn3-
21	RxIn3+
22	GND
23	NC
24	NC
25	GND
26	NC
27	LVBIT
28	NC
29	VSP(5.5V)
30	NC
31	LEDK-
32	LEDK-
33	NC
34	VSN(-5.5V)
35	NC
36	VGL(-10V)
37	NC
38	VGH(18V)
39	LEDA+
40	LEDA+

CIRCUIT DIAGRAM

LCM NOTES:
1. DISPLAY TYPE: 10.1 INCH TFT /Normally Black
2. BACKLIGHT: 45 CHIP WHITE LED ,5S9P, VF =16.2V; IF=270mA
3. OPERATING TEMP: -20°C~+70°C
4. STORAGE TEMP: -30°C~+80°C
5. LCD IC : -
6.Luminance: 500cd/m2(TYP)
7.LED Life Time :50000 Hours
8. "(" reference dimension "*"critical dimension
9. RoHS Compliant

REV.	DATE	MODIFICATION
1.0	2020.05.15	First issue

INTERFACE	LVDS Interface
	FPC Connector FH12-40S-0.5HS(55)
VIEWING DIRECTION	ALL
DIRECTION	ALL
Gray	

深圳市思迪科科技有限公司
SHENZHEN CDTECH ELECTRONICS

Material code:	
PART NO.	S10.1HWX33HD
REV.	V1.0
SHEET OF	1/1
TOLERANCE UNLESS SPECIFIED	±0.2
SCALE	1:1
UNIT	mm
PROJECTION	3rd ANGLE
DWN	Li Huang 2020.05.15
CHKD	HZX 2020.05.15

9 Packing

Packing Method

TBD

10 Precautions for Use of LCD modules

10.1 Handling Precautions

10.1.1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketene

10.1.6. Do not attempt to disassemble the LCD Module.

10.1.7. If the logic circuit power is off, do not apply the input signals.

10.1.8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

10.1.8.1. Be sure to ground the body when handling the LCD Modules.

10.1.8.2. Tools required for assembly, such as soldering irons, must be properly ground.

10.1.8.3. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

10.1.8.4. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage Precautions

10.2.1. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2. The LCD modules should be stored under the storage temperature range If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ～ 40℃ Relatively humidity: ≤80%

10.2.3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.